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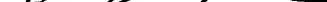

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				Complete if Known	
				Application Number	
				Filing Date	04/24/2001
				First Named Inventor	SANJAY AGARWAL
				Group Art Unit	
				Examiner Name	Pam 2183
				Attorney Docket Number	

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				Application Number	
				Filing Date 04/24/2001	
				First Named Inventor SANJAY AGARWAL	
				Group Art Unit 2183	
				Examiner Name Jan	
Sheet	1	of	1	Attorney Docket Number	

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
C		Douglas J Smith - HDL Chip Design. Pages 286-287.	
n		SIEMENS On-Chip Peripheral Components. Pages 126-133	
h		Kai Hwang - Advanced Computer Architecture: Parallelism, Scalability, Programmability. Pages 161-162	
a		David A. Patterson & John L. Hennessy - Computer Architecture - A Quantitative approach. Pages 10-12	
w		Electronic Engineering Times - Article by Charles J. Murray, dated April 16, 2001. Title "Internet connectivity energizes 8-bit MCUs.	

Examiner Signature		Date Considered	
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